

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-16 (canceled)

Claim 17 (new): A semiconductor device comprising:

a plurality of first terminals for respective capacitor connections;

a second terminal for supplying a power supply voltage;

a third terminal for supply a ground voltage; and

a voltage booster circuit electrically connecting said first, second and third terminals, the voltage booster boosts an input voltage by using a capacitor and generates a boosted voltage higher than said input voltage, the voltage booster circuit having

an input terminal at which the input voltage is applied,

an output terminal at which the boosted voltage is outputted,

a plurality of N channel transistors electrically connected in series

between said input terminal and said output terminal,

a plurality of first P channel transistors each of which electrically connected with corresponding ones of said plurality of N channel transistors in parallel, and

a controlling circuit that outputs control signals each of which controlling a conductive state of corresponding ones of said N channel transistors and corresponding ones of said P channel transistors,

wherein the plurality of first terminals are respectively electrically connected to first electrodes of corresponding ones of said N channel transistors and first electrodes of corresponding ones of said first P channel transistors corresponding to said corresponding one of N channel transistors,

wherein each of said N channel transistors is controlled to be in a conductive state different from a conductive state of an adjacent one of said N channel transistors, by said control signals, and

wherein each of said P channel transistors is controlled to be in a conductive state corresponding to the conductive state of a corresponding one of said N channel transistors electrically connected thereto in parallel, by said control signals.

Claim 18 (new): The semiconductor device as claimed in claim 17, the boosted voltage generated by the voltage booster circuit is used as a power supply voltage for a liquid crystal display.

Claim 19 (new): The semiconductor device as claimed in claim 17, the voltage booster circuit further comprising an activation circuit that sets a voltage level of the control signals so that said N channel transistors and said first P channel transistors are set in a non-conductive state according to an activation signal.

Claim 20 (new): The semiconductor device as claimed in claim 17, wherein the controlling circuit comprises:

a fixing circuit that fixes a voltage level of a control signal supplied to a gate electrode of a corresponding one of said N channel transistors and a gate electrode of a corresponding one of said first P channel transistors which is connected to the corresponding one of said N channel transistors, according to selection signals input thereto.

Claim 21 (new): The semiconductor device as claimed in claim 17, the voltage booster circuit further comprising a second P channel transistor connected in series between said output terminal and one of said N channel transistors, having a conductive state controlled by said controlling circuit.

Claim 22 (new): The semiconductor device as claimed in claim 17, further comprising:

a voltage supply circuit which outputs first and second voltages, the first and second voltages have substantively complementary voltage levels with respect to each other; and

a plurality of fourth terminals each of which receiving one of the first and second voltages,

wherein each of a plurality of capacitors are respectively electrically connected between one of the first terminals and one of the fourth terminals.

Claim 23 (new): The semiconductor device as claimed in claim 18, further comprising:

a voltage supplying circuit which outputs first and second voltages, the first and second voltages have substantively complementary voltage levels with respect to each other; and

a plurality of fourth terminals each of which receiving one of the first and second voltages,

wherein each of a plurality of capacitors are respectively electrically connected between one of the first terminals and one of the fourth terminals.